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What is claimed is:

1. An electro-luminescence display, comprising:
an electro-luminescence panel having a display area
and a non-display area;
driving circuit boards for applying driving signals
to a gate line and a data line provided at the
electro-luminescence panel; and
tape carrier packages connecting the driving circuit
boards and the electro-luminescence panel in a planar state
wherein the tape carrier packages and the driving circuit
boards are disposed in a common plane.
2. The electro-luminescence display according to claim
1, wherein the driving circuit boards include:
a gate driving circuit for applying driving signals
to the gate lines; and
a data driving circuit for applying driving signals
to the data lines.
3. The electro-luminescence display according to claim
1, wherein the driving circuit boards include a plurality
of output pads electrically connected to the tape carrier
packages.
4. The electro-luminescence display according to claim
3, wherein the electro-luminescence panel includes a
plurality of input pads that are provided at the non-
display area and electrically connected to the tape carrier
packages.
5. The electro-luminescence display according to claim

4, wherein each of the tape carrier packages includes:
first pads connected to the output pads of the driving
circuit boards; and
second pads connected to the input pads of the
electro-luminescence display.

6. The electro-luminescence display according to claim
2, wherein the tape carrier packages include:
a first group of tape carrier packages arranged
between the electro-luminescence panel and the gate
driving circuit; and
a second group of tape carrier packages arranged
between the electro-luminescence panel and the data
driving circuit.

7. The electro-luminescence display according to claim
1, wherein each of the tape carrier packages has a first
side for connecting the driving circuit boards to the
electro-luminescence panel and a second side for holding
a computer chip.

8. The electro-luminescence display according to claim
7, wherein a substantial portion of each of said tape
carrier packages is in a common plane with said driving
circuit boards.

9. The electro-luminescence display according to claim
7, wherein a substantial portion of each of said tape
carrier packages having a first portion disposed in a
common plane with said driving circuit boards and connected
to the electro-luminescence panel.

10. The electro-luminescence display according to claim 9, wherein each of said tape carrier packages has a second portion disposed in a contiguous plane to the common plane of said electro-luminescence panel and said first portion.